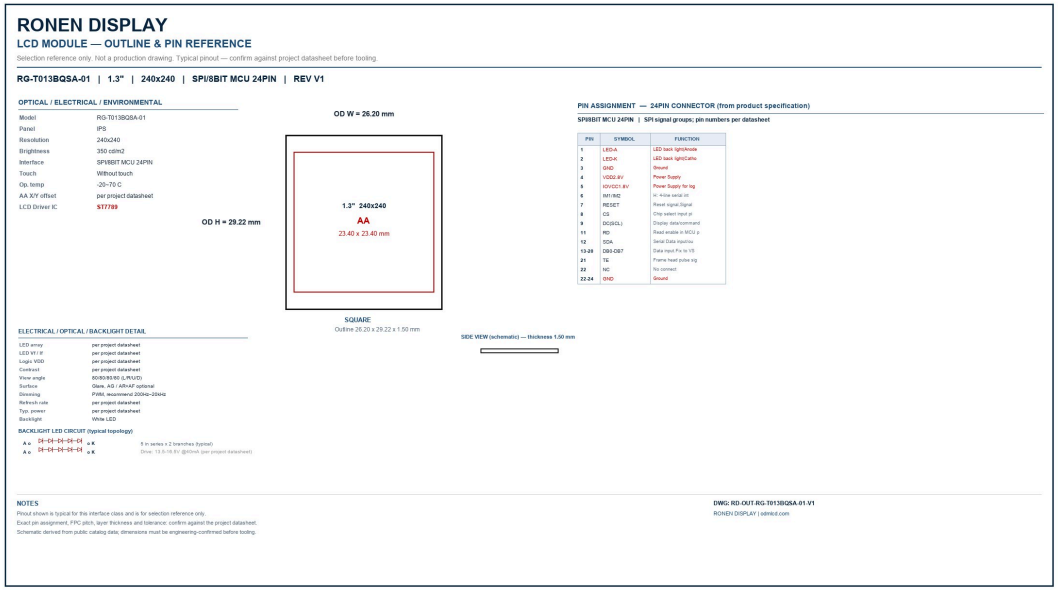
RONEN DISPLAY

LCD Module Datasheet

RG-T013BQSA-01

SPECIFICATION

Display Size	1.3 inch
Resolution	240x240
LCD Type / Shape	IPS Square
Luminance	350 cd/m2
Module Interface	SPI/8BIT MCU 24PIN
Touch Option	Without touch
Active Area (mm)	23.40x23.40
Outline Dim. (mm)	26.20x29.22x1.50
Operating Temp. (°C)	-20 ~ +70 degC
Storage Temp. (°C)	-30 ~ +80 degC


RONEN DISPLAY
LCD MODULE — OUTLINE & PIN REFERENCE
Selection reference only. Not a production drawing. Typical pinout — confirm against project datasheet before tooling.
RG-T013BQSA-01 | 1.3" | 240x240 | SPI/8BIT MCU 24PIN | REV V1
OPTICAL / ELECTRICAL / ENVIRONMENTAL
Model RG-T013BQSA-01
Panel IPS
Resolution 240x240
Brightness 350 cd/m2
Interface SPI/8BIT MCU 24PIN
Touch Without touch
Op. temp -20~70 C
AA X/Y offset per project datasheet
LCD Driver IC ST7789
OD W = 26.20 mm
OD H = 29.22 mm
1.3" 240x240
AA
23.40 x 23.40 mm
SQUARE
Outline 26.20 x 29.22 x 1.50 mm
SIDE VIEW (schematic) — thickness 1.50 mm
PIN ASSIGNMENT — 24PIN CONNECTOR (from product specification)
SPI/8BIT MCU 24PIN | SPI signal groups; pin numbers per datasheet
PIN SYMBOL FUNCTION
1 LED-A LED back light/Anode
2 LED-K LED back light/Cathe
3 GND Ground
4 VDD2.8V Power Supply
5 IOVCC1.8V Power Supply for log
6 INT/IM2 IC 4-line serial int
7 RESET Reset signal,Signal
8 CS Chip select input pi
9 DC(SCL) Display data/command
11 RD Read enable in MCU p
12 SDA Serial Data input/ou
13-28 DB0-DB7 Data input,Fo to V3
21 TE Frame head pulse sig
22 NC No connect
23-24 GND Ground
ELECTRICAL / OPTICAL / BACKLIGHT DETAIL
LED array per project datasheet
LED Vf / If per project datasheet
Logic VDD per project datasheet
Contrast per project datasheet
View angle 60/60/80/80 (L/R/U/D)
Surface Glare, AG / AR+AF optional
Dimming PWM, recommend 200Hz~20kHz
Refresh rate per project datasheet
Typ. power per project datasheet
Backlight White LED
BACKLIGHT LED CIRCUIT (typical topology)
A o D4 D4 D4 D4 D4 o K
A o D4 D4 D4 D4 D4 o K
8 in series x 2 branches (typical)
Drive: 19.5~18.5V @20mA (per project datasheet)
NOTES
Pinout shown is typical for this interface class and is for selection reference only.
Exact pin assignment, FPC pitch, layer thickness and tolerance: confirm against the project datasheet.
Schematic derived from public catalog data; dimensions must be engineering-confirmed before tooling.
DWG: RD-OUT-RG-T013BQSA-01-V1
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Outline drawing - for reference only; confirm with latest datasheet.

Notes

Values are typical at 25 degC unless otherwise specified. Full drawing, pin assignment and qualification data available on request. Sales terms are confirmed per project.

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