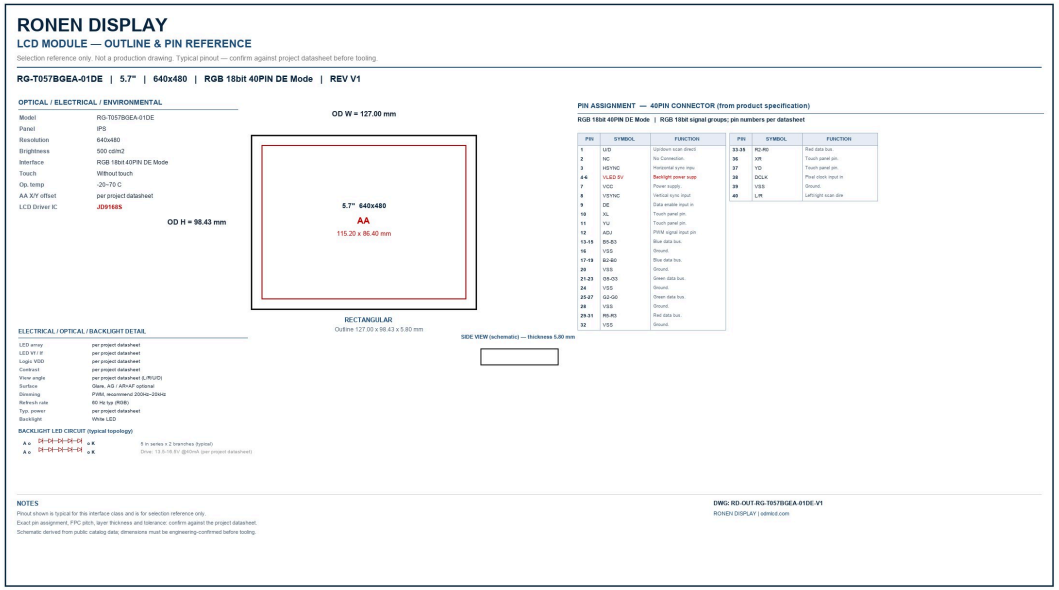
RONEN DISPLAY

LCD Module Datasheet

RG-T057BGEA-01DE

SPECIFICATION

Display Size	5.7 inch
Resolution	640x480
LCD Type / Shape	IPS
Luminance	500 cd/m2
Module Interface	RGB 18bit 40PIN DE Mode
Touch Option	Without touch
Active Area (mm)	115.20x86.40
Outline Dim. (mm)	127.00x98.43x5.80
Operating Temp. (°C)	-20 ~ +70 degC
Storage Temp. (°C)	-30 ~ +80 degC


RONEN DISPLAY

LCD MODULE — OUTLINE & PIN REFERENCE

Selection reference only. Not a production drawing. Typical pinout — confirm against project datasheet before tooling.

RG-T057BGEA-01DE | 5.7" | 640x480 | RGB 18bit 40PIN DE Mode | REV V1

OPTICAL / ELECTRICAL / ENVIRONMENTAL

Model	RG-T057BGEA-01DE
Panel	IPS
Resolution	640x480
Brightness	500 cd/m2
Interface	RGB 18bit 40PIN DE Mode
Touch	Without touch
Op. temp	-20~70 C
AA X/Y offset	per project datasheet
LCD Driver IC	JD9168S

OD W = 127.00 mm

OD H = 98.43 mm

5.7" 640x480

AA

115.20 x 86.40 mm

RECTANGULAR

Outline 127.00 x 98.43 x 5.80 mm

SIDE VIEW (schematic) — thickness 5.80 mm

ELECTRICAL / OPTICAL / BACKLIGHT DETAIL

LED array	per project datasheet
LED Vf / If	per project datasheet
Logic VDD	per project datasheet
Contrast	per project datasheet
View angle	per project datasheet (L/R/U/D)
Surface	Glare, AG / AR+AF optional
Dimming	PWM, recommend 200Hz~20kHz
Refresh rate	60 Hz typ (RGB)
Typ. power	per project datasheet
Backlight	White LED

BACKLIGHT LED CIRCUIT (typical topology)

A o — D1 D2 D3 D4 D5 D6 — o K

A o — D7 D8 D9 D10 D11 D12 — o K

6 in series × 2 branches (typical)

Drive: 19.5–38.5V @20mA (per project datasheet)

PIN ASSIGNMENT — 40PIN CONNECTOR (from product specification)

RGB 18bit 40PIN DE Mode | RGB 18bit signal groups; pin numbers per datasheet

PIN	SYMBOL	FUNCTION
1	U/D	Up/down scan direct
2	NC	No Connection
3	HSYNC	Horizontal sync inpu
4-6	VLED 5V	Backlight power supp
7	VCC	Power supply
8	VSYNC	Vertical sync input
9	DE	Data enable input in
10	XL	Touch panel pin
11	YU	Touch panel pin
12	ADJ	PWM signal input pin
13-15	B5-B3	Blue data bus
16	VSS	Ground
17-19	B2-B0	Blue data bus
20	VSS	Ground
21-23	G5-G3	Green data bus
24	VSS	Ground
25-27	G2-G0	Green data bus
28	VSS	Ground
29-31	R5-R3	Red data bus
32	VSS	Ground

PIN	SYMBOL	FUNCTION
33-35	R2-R0	Red data bus
36	XR	Touch panel pin
37	YD	Touch panel pin
38	DCLK	Pixel clock input in
39	VSS	Ground
40	L/R	Left/right scan dire

NOTES

Pinout shown is typical for this interface class and is for selection reference only.

Exact pin assignment, FPC pitch, layer thickness and tolerance: confirm against the project datasheet.

Schematic derived from public catalog data; dimensions must be engineering-confirmed before tooling.

DWG: RD-OUT-RG-T057BGEA-01DE-V1

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Outline drawing - for reference only; confirm with latest datasheet.

Notes

Values are typical at 25 degC unless otherwise specified. Full drawing, pin assignment and qualification data available on request. Sales terms are confirmed per project.

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