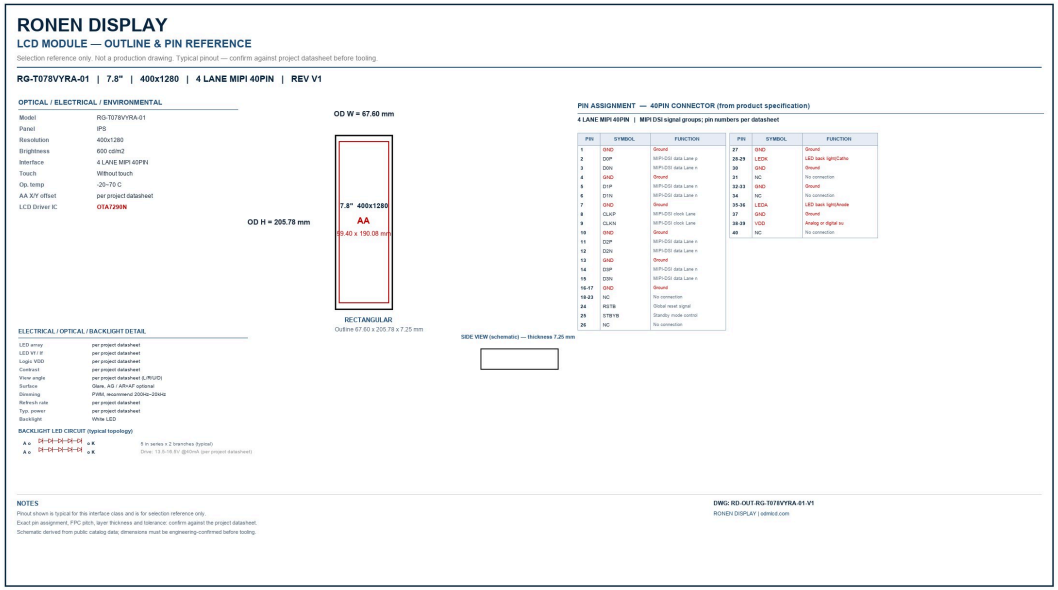
RONEN DISPLAY

LCD Module Datasheet

RG-T078VYRA-01

SPECIFICATION

Display Size	7.8 inch
Resolution	400x1280
LCD Type / Shape	IPS
Luminance	600 cd/m2
Module Interface	4 LANE MIPI 40PIN
Touch Option	Without touch
Active Area (mm)	59.40x190.08
Outline Dim. (mm)	67.60x205.78x7.25
Operating Temp. (°C)	-20 ~ +70 degC
Storage Temp. (°C)	-30 ~ +80 degC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LCD MODULE — OUTLINE & PIN REFERENCE

Selection reference only. Not a production drawing. Typical pinout — confirm against project datasheet before tooling.

RG-T078VYRA-01 | 7.8" | 400x1280 | 4 LANE MIPI 40PIN | REV V1

OPTICAL / ELECTRICAL / ENVIRONMENTAL

Model	RG-T078VYRA-01
Panel	IPS
Resolution	400x1280
Brightness	600 cd/m2
Interface	4 LANE MIPI 40PIN
Touch	Without touch
Op. temp	-20~70 C
AA X/Y offset	per project datasheet
LCD Driver IC	OTA7290N

OD W = 67.60 mm

OD H = 205.78 mm

7.8" 400x1280

AA

59.40 x 190.08 mm

RECTANGULAR

Outline 67.60 x 205.78 x 7.25 mm

SIDE VIEW (schematic) — thickness 7.25 mm

PIN ASSIGNMENT — 40PIN CONNECTOR (from product specification)

4 LANE MIPI 40PIN | MIPI DSI signal groups; pin numbers per datasheet

PIN	SYMBOL	FUNCTION
1	GND	Ground
2	D0P	MIPI-DSI data Lane p
3	D0N	MIPI-DSI data Lane n
4	GND	Ground
5	D1P	MIPI-DSI data Lane n
6	D1N	MIPI-DSI data Lane n
7	GND	Ground
8	CLKP	MIPI-DSI clock Lane
9	CLKN	MIPI-DSI clock Lane
10	GND	Ground
11	D2P	MIPI-DSI data Lane n
12	D2N	MIPI-DSI data Lane n
13	GND	Ground
14	D3P	MIPI-DSI data Lane n
15	D3N	MIPI-DSI data Lane n
16-17	GND	Ground
18-22	NC	No connection
24	RSTB	Global reset signal
23	STBYB	Standby mode control
25	NC	No connection
27	GND	Ground
28-29	LEDK	LED back light(Cathe
30	GND	Ground
31	NC	No connection
32-33	GND	Ground
34	NC	No connection
35-36	LEDA	LED back light(Anode
37	GND	Ground
38-39	VDD	Analog or digital su
40	NC	No connection

ELECTRICAL / OPTICAL / BACKLIGHT DETAIL

LED array	per project datasheet
LED Vf / If	per project datasheet
Logic VDD	per project datasheet
Contrast	per project datasheet
View angle	per project datasheet (L/R/U/D)
Surface	Glare, AG / AR+AF optional
Dimming	PWM, recommend 200Hz~20kHz
Refresh rate	per project datasheet
Typ. power	per project datasheet
Backlight	White LED

BACKLIGHT LED CIRCUIT (typical topology)

A o — [8 × 4 = 32 LEDs] — o K

A o — [8 × 4 = 32 LEDs] — o K

8 in series × 2 branches (typical)

Drive: 19.5–38.5V @60mA (per project datasheet)

NOTES

Pinout shown is typical for this interface class and is for selection reference only.

Exact pin assignment, FPC pitch, layer thickness and tolerance: confirm against the project datasheet.

Schematic derived from public catalog data; dimensions must be engineering-confirmed before tooling.

DWG: RD-OUT-RG-T078VYRA-01-V1

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Outline drawing - for reference only; confirm with latest datasheet.

Notes

Values are typical at 25 degC unless otherwise specified. Full drawing, pin assignment and qualification data available on request. Sales terms are confirmed per project.

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