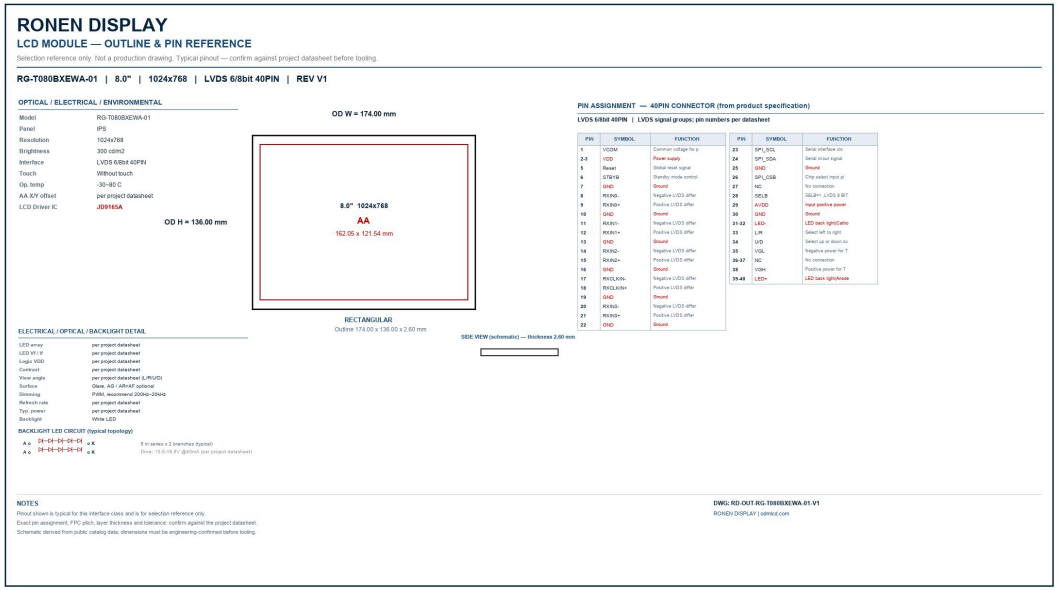
RONEN DISPLAY

LCD Module Datasheet

RG-T080BXEWA-01

SPECIFICATION

Display Size	8.0 inch
Resolution	1024x768
LCD Type / Shape	IPS
Luminance	300 cd/m2
Module Interface	LVDS 6/8bit 40PIN
Touch Option	Without touch
Active Area (mm)	162.05x121.54
Outline Dim. (mm)	174.00x136.00x2.60
Operating Temp. (°C)	-30 ~ +80 degC
Storage Temp. (°C)	-30 ~ +80 degC


RONEN DISPLAY
LCD MODULE — OUTLINE & PIN REFERENCE
Selection reference only. Not a production drawing. Typical pinout — confirm against project datasheet before tooling.
RG-T080BXEWA-01 | 8.0" | 1024x768 | LVDS 6/8bit 40PIN | REV V1
OPTICAL / ELECTRICAL / ENVIRONMENTAL
Model RG-T080BXEWA-01
Panel IPS
Resolution 1024x768
Brightness 300 cd/m2
Interface LVDS 6/8bit 40PIN
Touch Without touch
Op. temp -30~80 C
AA X/Y offset per project datasheet
LCD Driver IC JD9165A
OD W = 174.00 mm
OD H = 136.00 mm
8.0" 1024x768
AA
162.05 x 121.54 mm
RECTANGULAR
Outline 174.00 x 136.00 x 2.60 mm
SIDE VIEW (schematic) — thickness 2.60 mm
PIN ASSIGNMENT — 40PIN CONNECTOR (from product specification)
LVDS 6/8bit 40PIN | LVDS signal groups; pin numbers per datasheet
PIN SYMBOL FUNCTION
1 VCOM Common voltage for p
2-3 VDD Power supply
5 Reset Global reset signal
6 STBYB Standby mode control
7 GND Ground
8 RXIN0- Negative LVDS differ
9 RXIN0+ Positive LVDS differ
10 GND Ground
11 RXIN1- Negative LVDS differ
12 RXIN1+ Positive LVDS differ
13 GND Ground
14 RXIN2- Negative LVDS differ
15 RXIN2+ Positive LVDS differ
16 GND Ground
17 RXCLKIN- Negative LVDS differ
18 RXCLKIN+ Positive LVDS differ
19 GND Ground
20 RXIN3- Negative LVDS differ
21 RXIN3+ Positive LVDS differ
22 GND Ground
23 SPI_SCL Serial interface clo
24 SPI_SDA Serial in/out signal
25 GND Ground
26 SPI_CSB Chip select input pi
27 NC No connection
28 SELB SELB=1: LVDS 6 BIT
29 AVDD Input positive power
30 GND Ground
31-32 LED- LED back light/Cathe
33 L/R Select left to right
34 U/D Select up or down sc
35 VGL Negative power for T
36-37 NC No connection
38 VGH Positive power for T
39-40 LED+ LED back light/Anode
ELECTRICAL / OPTICAL / BACKLIGHT DETAIL
LED array per project datasheet
LED Vf / If per project datasheet
Logic VDD per project datasheet
Contrast per project datasheet
View angle per project datasheet (L/R/U/D)
Surface Glare, AG / AR+AF optional
Dimming PWM, recommend 200Hz~20kHz
Refresh rate per project datasheet
Typ. power per project datasheet
Backlight White LED
BACKLIGHT LED CIRCUIT (typical topology)
A o
K
8 in series x 2 branches (typical)
Drive: 19.5~38.5V @60mA (per project datasheet)
NOTES
Pinout shown is typical for this interface class and is for selection reference only.
Exact pin assignment, FPC pitch, layer thickness and tolerance: confirm against the project datasheet.
Schematic derived from public catalog data; dimensions must be engineering-confirmed before tooling.
DWG: RD-OUT-RG-T080BXEWA-01-V1
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Outline drawing - for reference only; confirm with latest datasheet.

Notes

Values are typical at 25 degC unless otherwise specified. Full drawing, pin assignment and qualification data available on request. Sales terms are confirmed per project.

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